



AT9-98-535

PATENT

#410
12-19-03

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:	Before the Examiner:
Balaram Sinharoy	:	Aimee J. Li
Serial No.: 09/631,726	:	Group Art Unit: 2183
Filed: August 3, 2000	:	
Title: BRANCH PREDICTION CIRCUITS:	:	IBM Corporation
AND METHODS AND SYSTEMS USING:	:	Intellectual Property Law
THE SAME	:	11400 Burnet Road
	:	Austin, Texas 78758

RECEIVED

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REPLY UNDER 37 C.F.R. § 1.111

Technology Center 2100

Mail Stop Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action having a mailing date of August 27, 2003 (Paper No. 3), having a three-month shortened statutory period for response set to expire on November 28, 2003, please reconsider the rejections of the claims in view of the following remarks:

CERTIFICATION UNDER 37 C.F.R. § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on November 28, 2003.

Signature

Robert A. Voigt, Jr.
(Printed name of person certifying)

IN THE CROSS-REFERENCE TO RELATED APPLICATIONS

- (1) Please rewrite the cross-reference to related applications as follows:
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Al The present invention is related to the following U.S. Patent Applications which are incorporated herein by reference:

Serial No. 09/631,725 (Attorney Docket No. AUS9-2000-0481-US1) entitled "Global History Vector Recovery Circuits and Methods and Systems Using the Same," filed concurrently herewith;

Serial No. 09/435,065 (Attorney Docket No. AT9-98-545) entitled "Circuits and Methods for Prefetching Instructions and Data Processing System Using Same"; and

Serial No. 09/475,030 (Attorney Docket No. AT9-98-544) entitled "Circuits, Systems and Methods for Performing Branch Predictions by Selectively Accessing Bimodal and Fetch-Based Branch History Tables."
